MUTTAHIDAH

Senior Analog and Mixed-Signal IC Design Engineer

Job categories IC Design
Vacancy code VA/2023/B5025/19553
Department/office Mutahhidah, Research & Development Department
Duty station Luxembourg
Application period 25-NSep-2023 to 25 Dec-2023

Applications to vacancies must be received before midnight Luxembourg time (CET) on the closing date of the announcement.

Background Information - Muttahidah

Muttahidah develops Analog & Mixed-signal ICs and full-custom ASIC (application- specific integrated circuit). Our founders have many years of experience in analog and mixed- signal integrated circuit design. As a specialized supplier, Muttahidah offers the full range of design services in the field of IC, ASIC and Electronic System development, i.e., from feasibility studies, standard ICs product, custom application-specific modules, custom ICs to System miniaturization or turnkey electronic solutions for consumer, industry, scientific and medical markets.

Main Duties/Responsibilities

- Perform the daily development of ADC and DAC related projects and products
- Participate in all aspects of the design, from concept to production silicon, with some customer engagement
- Involved in specification, architectural development, transistor-level design, SPICE, Matlab modeling and simulation, layout and layout supervision, post-layout simulation, chip-level verification, and lab validation
- Interact and participate with a cross functional team including Product, Test, Marketing, and Application Engineers as needed for a successful execution of a product
- Successful integration of blocks into larger mixed-signal systems
- Mentor junior design team members
- Document, present, and participate in project meetings

Qualifications

- Fluency in Bachelor's Degree, Master's or PhD's Degree preferred.

Experience

- Master's with 8+ years or PhD with 5+ years of experience in analog/mixed-signal IC development
- 1+ years small team management experience preferred

Skills

- Expertise in:
  - ✓ Data converter architecture and design
  - ✓ Transistor-level analog design with solid understanding of precision circuit requirements and topologies
  - ✓ Analog building blocks: references, amps, oscillators, LDO's
  - ✓ RTL and custom logic in support of high-performance data paths in mixed-signal designs
  - Advanced analog and digital simulation techniques using Cadence
  - System level Mixed Mode verification expertise
  - Organized, thorough, and detail-oriented with strong communication skills
  - Self-motivated with strong analytical and problem solving skills
  - Experience working with cross-functional teams
  - Excellent written and oral communication skills in English, knowledge of Chinese, Dutch, French or German will be an advantage

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